Amendments to the Specification

Please amend the paragraph at p. 4, ll. 11 – 29 as follows:

The present invention relates to timing information based on a transmitter System Time Clock (STC) counter that is periodically inserted by the transmitter into a transport stream. There are various circumstances in which it is highly desirable to account for the delay between reception of a packet from the transmitter and actual applications using that packet. This is the case, for example, in applications where a direct memory access engine is used to transfer elementary streams from the receiver, such as described in the concurrently filed and commonly assigned ~~application~~ U.S. Pat. Appl. No. 09/651,539, entitled "MULTITHREADED DIRECT MEMORY ACCESS ENGINE FOR BROADCAST DATA DEMULTIPLEX OPERATIONS," ~~(Attorney Docket No. 19927-000510US)~~ having Thomas Gene Adams and Gene Maine as coinventors, which is herein incorporated by reference for all purposes. Such applications may also include flexibile media access control as described in the concurrently filed and commonly assigned ~~application~~ U.S. Pat. Appl. No. 09/649,792, entitled "FLEXIBLE MEDIA ACCESS CONTROL AND SECTION FILTER HARDWARE ENGINE FOR SATELLITE DATA RECEIVER," (Attorney Docket No. 19927-000610US) having Thomas G. Adams and Randy R. Fuller as coinventors, which is also herein incorporated by reference for all purposes. Since there is considerable variability in the delay through the steps of demultiplexing the elementary streams and performing the direct memory access through the firmware and software layers, it is desirable to use the calculation of the delay to synchronize the operations.